**Session Program**

**Feb 4 – 10, 2026**

# Bootcamp RTL, IP Integration, and SoC Signoff

# Introduction to RTL, IP Integration, and SoC Signoff

UIET Kathua, 1

# Wed, February 4

10:00 AM

11:00 AM

## Introduction to RTL, IP Integration, and SoC Signoff: Introduction

**Session** | **Location:** UIET Kathua, 1 | **Convener:** Shailendra Krishan Sharma

12:00 PM

1:00 PM

## Introduction to RTL, IP Integration, and SoC Signoff: And Gate in Verilog

**Session** | **Location:** UIET Kathua, 1 | **Convener:** Shailendra Krishan Sharma