Session Program

Feb 4 – 10, 2026

Bootcamp RTL, IP Integration, and SoC Signoff

Verilator Half Adder

UIET Kathua, 1

Wed, February 4

3:30 PM

4:30 PM

Verilator Half Adder

Session | **Location:** UIET Kathua, 1 | **Convener:** Shailendra Krishan Sharma