Session Program

Feb 9 – 13, 2026

Bootcamp: RTL, IP Integration, and SoC Signoff

Low Power, Multi-Clock & Reset Design

NIELIT Chitradurga, 001

Thu, February 12

9:30 AM

4:00 PM

Low Power, Multi-Clock & Reset Design: Low Power, Multi-Clock & Reset Design

Session | **Location:** NIELIT Chitradurga, 001 | **Convener:** Viswanath Naik S