Session Program

Feb 9 – 13, 2026

Bootcamp: RTL, IP Integration, and SoC Signoff

Mini-SoC Project & Debugging

NIELIT Chitradurga, 001

Fri, February 13

9:30 AM

4:00 PM

Mini-SoC Project & Debugging: Mini-SoC Project & Debugging

Session | **Location:** NIELIT Chitradurga, 001 | **Convener:** Viswanath Naik S